

部品番号	ピン番号	信号名	i.MX8ULP					基板上のPull-Up/Pull-Down とコンデンサ	マルチプレクス機能(i.MX8ULPの信号名で表記)								
			ピン名	電圧グループ	リセット解除後の信号状態				GPIO	FXIO	ADC	SPI	UART	I2C	TPM	I2S	CAN
					機能	In/Out	Value										
CON10	1	VIN				-											
	2	VIN				-											
	3	GND				-											
	4	GND				-											
	5	VDD 5V															
	6	USBDP_DN2	-														
	7	GND															
	8	USBDN_DN2	-														
	9	ONOFF	ONOFF	VDD_VBAT18	ONOFF	In	Pull-Up	100kΩ Pull-Up									
	10	VDD 1V8															
	11	PTC13	PTC13	VDD 1V8		-	Hi-Z		PTC13			LPSPi3_SIN			TPM3_CH1	I2S1_TXD3	
	12	LPI2C7_SCL	PTF4	VDD 1V8		-	Pull-Up	4.7kΩ Pull-Up						LPI2C7_SCL			
	13	PTC14	PTC14	VDD 1V8		-	Hi-Z		PTC14			LPSPi3_SOUT			TPM3_CH2	I2S1_TXD2	
	14	LPI2C7_SDA	PTF5	VDD 1V8		-	Pull-Up	4.7kΩ Pull-Up						LPI2C7_SDA	TPM7_CH0		
	15	PTC15	PTC15	VDD 1V8		-	Hi-Z		PTC15			LPSPi3_SCK			TPM3_CH3	I2S1_RX_BCLK	
	16	PTF6	PTF6	VDD 1V8		-	Hi-Z		PTF6				LPUART7_TX		TPM7_CH1		
	17	PTC16	PTC16	VDD 1V8		-	Hi-Z		PTC16			LPSPi3_PCS0			TPM3_CH4	I2S1_RX_FS	
	18	PTF7	PTF7	VDD 1V8		-	Hi-Z		PTF7				LPUART7_RX		TPM7_CH2		
	19	PTC17	PTC17	VDD 1V8		-	Hi-Z		PTC17			LPSPi3_PCS1			TPM3_CH5	I2S1_RXD0	
	20	PTC20	PTC20	VDD 1V8		-	Hi-Z		PTC20							I2S1_TXD1	
	21	PTC18	PTC18	VDD 1V8		-	Hi-Z		PTC18			LPSPi3_PCS2				I2S1_RXD1	
	22	PTC21	PTC21	VDD 1V8		-	Hi-Z		PTC21							I2S1_TX_BCLK	
	23	PTC19	PTC19	VDD 1V8		-	Hi-Z		PTC19			LPSPi3_PCS3				I2S1_TXD0	
	24	PTC22	PTC22	VDD 1V8		-	Hi-Z		PTC22							I2S1_TX_FS	
	25	PTA8	PTA8	VDD 1V8		-	Hi-Z		PTA8	FXIO0_D0	ADC1_CH0A	LPSPi1_PCS1	LPUART1_CTS_B	LPI2C0_SCL	TPM1_CH0		
	26	PTC23	PTC23	VDD 1V8		-	Hi-Z		PTC23							I2S1_MCLK	
	27	PTA9	PTA9	VDD 1V8		-	Hi-Z		PTA9	FXIO0_D1	ADC1_CH0B	LPSPi1_PCS2	LPUART1_RTS_B	LPI2C0_SDA	TPM1_CH1	I2S1_RX_BCLK	
	28	PTA11	PTA11	VDD 1V8		-	Hi-Z		PTA11	FXIO0_D3	ADC1_CH1B		LPUART1_RX			I2S1_RXD0	
	29	PTA10	PTA10	VDD 1V8		-	Hi-Z		PTA10	FXIO0_D2	ADC1_CH1A	LPSPi1_PCS3	LPUART1_TX			I2S1_RX_FS	
	30	PTA12	PTA12	VDD 1V8		-	Hi-Z		PTA12	FXIO0_D4	ADC1_CH2A	LPSPi1_SIN				I2S1_RXD1	CAN0_TX
	31	GND															
	32	PTA13	PTA13	VDD 1V8		-	Hi-Z		PTA13	FXIO0_D5	ADC1_CH2B	LPSPi1_SOUT				I2S1_MCLK	CAN0_RX
	33	VDD 3V3															
	34	PTA14	PTA14	VDD 1V8		-	Hi-Z		PTA14	FXIO0_D6	ADC1_CH3A	LPSPi1_SCK				I2S1_TX_BCLK	

[a] 出荷状態のソフトウェアでの入出力方向は次のとおりです。

