

ピン番号	信号名	iMX257 ピン名	リセット解除後の信号状態				基板上の PU/PD	ブートローダー起動後の信号状態 ³⁾				基板上の PU/PD	マルチプレックス機能																				
			I/O	Pin State	Open Drain	Pull/Keeper		Mux Mode	I/O	Pin State	Open Drain		Pull/Keeper	GPIO	USB	UART1	UART3	UART4	UART5	CSP12	CSP13	I2C2	I2C3	CAN1	CAN2	KPP	SD2	AUD3	AUD5	LCDC	SLCDC	PWM	1-Wire
1	GND																																
2	GND																																
3	EXTIO22	GPIO C	In	-	Disable	100kΩ PD	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 2						SS2		SCL			TX	COL4						PWM04	
4	EXTIO23	GPIO D	In	-	Disable	100kΩ PD	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 3							SS2		SDA		RX	COL5							
5	PWM01	PWM	In	-	Disable	100kΩ PD	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO1 26																		PWM01	
6	EXTIO1	RTCK	Out	Low	Disable	Disable	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO13 14																		LINE	
7	EXTIO35	UART1_RXD	In	-	Disable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO4 22				RXD															
8	EXTIO36	UART1_TXD	Hi-Z	-	-	-	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO4 23				TXD															
9	EXTIO37	UART1_RTS	In	-	Disable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO4 24				RTS															
10	EXTIO38	UART1_CTS	Hi-Z	-	-	-	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO4 25				CTS															
11	EXTIO12	CSI D9	In	-	Disable	Keeper	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO4 21						SS3													
12	EXTIO3	CSI D2	In	-	Disable	Keeper	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 27							MOSI												
13	EXTIO5	CSI D3	In	-	Disable	Keeper	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 28				TXD			MISO												
14	EXTIO7	CSI D4	In	-	Disable	Keeper	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 29				RTS			SCLK												
15	EXTIO9	CSI D5	In	-	Disable	Keeper	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 30				CTS			RDY												
16	EXTIO25	KPP ROW0	In	-	Disable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO2 29				DTR	RXD						ROW0								
17	EXTIO26	KPP ROW1	In	-	Disable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO2 30				DSR	TXD						ROW1								
18	EXTIO27	KPP ROW2	In	-	Disable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO2 31				DCD	RTS						ROW2								
19	EXTIO28	KPP ROW3	In	-	Disable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO3 0				RI	CTS						ROW3								
20	EXTIO29	KPP COL0	In	-	Enable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO3 1											COL0								
21	EXTIO30	KPP COL1	In	-	Enable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO3 2					RXD						COL1								
22	EXTIO31	KPP COL2	In	-	Enable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO3 3											COL2								
23	EXTIO32	KPP COL3	In	-	Enable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO3 4					RTS						COL3								
24	EXTIO33	GPIO A	In	-	Disable	Disable	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 0					CTS						ROW4							PWM02	
25	EXTIO34	GPIO R	In	-	Disable	100kΩ PD	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 1										SCL	TX	ROW5						PWM03	
26	GND																																
27	GND																																
28	+3.3V IO																																
29	+3.3V																																
30	+5V																																
31	DETECT CON1	-	-	-	-	-	1kΩ PD	-	-	-	-	-	1kΩ PD																				
32	LCD LD17	GPIO F	In	-	Disable	Disable	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 5																		LD17	
33	LCD LD16	GPIO E	In	-	Enable	100kΩ PU	-	GPIO	In	-	Disable	100kΩ PD	-	GPIO1 4					RXD													LD16	
34	LCD LD15	LD15	Out	Low	Disable	Disable	47kΩ PU	LCD	Out	Low	Disable	Disable	47kΩ PU												COL7							LD15	
35	LCD LD14	LD14	Out	Low	Disable	Disable	47kΩ PU	LCD	Out	Low	Disable	Disable	47kΩ PU												COL6							LD14	
36	LCD LD13	LD13	Out	Low	Disable	Disable	47kΩ PD	LCD	Out	Low	Disable	Disable	47kΩ PD												ROW7							LD13	
37	LCD LD12	LD12	Out	Low	Disable	Disable	47kΩ PD	LCD	Out	Low	Disable	Disable	47kΩ PD												ROW8							LD12	
38	LCD LD11	LD11	Out	Low	Disable	Disable	47kΩ PD	LCD	Out	Low	Disable	Disable	47kΩ PD					CTS														LD11	
39	LCD LD10	LD10	Out	Low	Disable	Disable	47kΩ PD	LCD	Out	Low	Disable	Disable	47kΩ PD					RTS														LD10	
40	LCD LD9	LD9	Out	Low	Disable	Disable	47kΩ PD	LCD	Out	Low	Disable	Disable	47kΩ PD					TXD														LD9	
41	LCD LD8	LD8	Out	Low	Disable	Disable	47kΩ PD	LCD	Out	Low	Disable	Disable	47kΩ PD																				LD8
42	LCD LD7	LD7	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO1 21																		LD7	
43	LCD LD6	LD6	Out	Low	Disable	Disable	47kΩ PU	GPIO	In	-	Disable	Disable	47kΩ PU	GPIO1 20																		LD6	
44	LCD LD5	LD5	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO1 19																		LD5	
45	LCD LD4	LD4	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO2 19																		LD4	
46	LCD LD3	LD3	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO2 18																		LD3	
47	LCD LD2	LD2	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO2 17																		LD2	
48	LCD LD1	LD1	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO2 16																		LD1	
49	LCD LD0	LD0	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO2 15																		LD0	
50	LCD OE ACD	OE ACD	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO1 25						SS0												OE ACD	
51	LCD VSYN	VSYN	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO1 23																			VSYN
52	LCD HSYN	HSYN	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO1 22																			HSYN
53	LCD LSCLK	LSCLK	Out	Low	Disable	Disable	47kΩ PD	GPIO	In	-	Disable	Disable	47kΩ PD	GPIO1 24																			CS
54	GND																																
55	PMIC ONOFF	-	-	-	-	-	-	-	-	-	-	-	-																				
56	USB VBUS																																
57	USB VBUS																																
58	GND																																
59	EXT USB HS_DP	USBPHY1_DP	Analog	-	-	-	-	-	-	-	-	-	-																				
60	EXT USB HS_DM	USBPHY1_DM	Analog	-	-	-	-	-	-	-	-	-	-																				

[b] loader-armadillo-iotg-std-v3.3.1.bin 以降