

32.6.163 SW_PAD_CTL_PAD_UART1_TX_DATA SW PAD Control Register (IOMUXC_SW_PAD_CTL_PAD_UART1_TX_DATA)

SW_PAD_CTL Register

Address: 20E_0000h base + 310h offset = 20E_0310h

Bit	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
R	Reserved															HYS
W																
Reset	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
R	PUS	PUE	PKE	ODE	Reserved			SPEED	DSE			Reserved		SRE		
W																
Reset	0	0	0	1	0	0	0	0	1	0	1	1	0	0	0	0

IOMUXC_SW_PAD_CTL_PAD_UART1_TX_DATA field descriptions

Field	Description
31–17 -	This field is reserved. Reserved
16 HYS	Hyst. Enable Field Select one out of next values for pad: UART1_TX_DATA 0 HYS_0_Hysteresis_Disabled — Hysteresis Disabled 1 HYS_1_Hysteresis_Enabled — Hysteresis Enabled
15–14 PUS	Pull Up / Down Config. Field Select one out of next values for pad: UART1_TX_DATA 00 PUS_0_100K_Ohm_Pull_Down — 100K Ohm Pull Down 01 PUS_1_47K_Ohm_Pull_Up — 47K Ohm Pull Up 10 PUS_2_100K_Ohm_Pull_Up — 100K Ohm Pull Up 11 PUS_3_22K_Ohm_Pull_Up — 22K Ohm Pull Up
13 PUE	Pull / Keep Select Field Select one out of next values for pad: UART1_TX_DATA 0 PUE_0_Keeper — Keeper 1 PUE_1_Pull — Pull
12 PKE	Pull / Keep Enable Field Select one out of next values for pad: UART1_TX_DATA 0 PKE_0_Pull_Keeper_Disabled — Pull/Keeper Disabled 1 PKE_1_Pull_Keeper_Enabled — Pull/Keeper Enabled
11 ODE	Open Drain Enable Field Select one out of next values for pad: UART1_TX_DATA

Table continues on the next page...

IOMUXC_SW_PAD_CTL_PAD_UART1_TX_DATA field descriptions (continued)

Field	Description
	0 ODE_0_Open_Drain_Disabled — Open Drain Disabled 1 ODE_1_Open_Drain_Enabled — Open Drain Enabled
10–8 -	This field is reserved. Reserved
7–6 SPEED	Speed Field Select one out of next values for pad: UART1_TX_DATA 00 SPEED_0_low_50MHz_ — low(50MHz) 01 SPEED_1_medium_100MHz_ — medium(100MHz) 10 SPEED_2_medium_100MHz_ — medium(100MHz) 11 SPEED_3_max_200MHz_ — max(200MHz)
5–3 DSE	Drive Strength Field Select one out of next values for pad: UART1_TX_DATA 000 DSE_0_output_driver_disabled_ — output driver disabled; 001 DSE_1_R0_260_Ohm__3_3V_150_Ohm_1_8V_240_Ohm_for_DDR_ — R0(260 Ohm @ 3.3V, 150 Ohm@1.8V, 240 Ohm for DDR) 010 DSE_2_R0_2 — R0/2 011 DSE_3_R0_3 — R0/3 100 DSE_4_R0_4 — R0/4 101 DSE_5_R0_5 — R0/5 110 DSE_6_R0_6 — R0/6 111 DSE_7_R0_7 — R0/7
2–1 -	This field is reserved. Reserved
0 SRE	Slew Rate Field Select one out of next values for pad: UART1_TX_DATA 0 SRE_0_Slow_Slew_Rate — Slow Slew Rate 1 SRE_1_Fast_Slew_Rate — Fast Slew Rate

32.6.164 SW_PAD_CTL_PAD_UART1_RX_DATA SW PAD Control Register (IOMUXC_SW_PAD_CTL_PAD_UART1_RX_DATA)

SW_PAD_CTL Register

Address: 20E_0000h base + 314h offset = 20E_0314h

Bit	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
R	Reserved															HYS
W																
Reset	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
R	PUS	PUE	PKE	ODE	Reserved			SPEED	DSE			Reserved		SRE		
W																
Reset	0	0	0	1	0	0	0	0	1	0	1	1	0	0	0	0

IOMUXC_SW_PAD_CTL_PAD_UART1_RX_DATA field descriptions

Field	Description
31–17 -	This field is reserved. Reserved
16 HYS	Hyst. Enable Field Select one out of next values for pad: UART1_RX_DATA 0 HYS_0_Hysteresis_Disabled — Hysteresis Disabled 1 HYS_1_Hysteresis_Enabled — Hysteresis Enabled
15–14 PUS	Pull Up / Down Config. Field Select one out of next values for pad: UART1_RX_DATA 00 PUS_0_100K_Ohm_Pull_Down — 100K Ohm Pull Down 01 PUS_1_47K_Ohm_Pull_Up — 47K Ohm Pull Up 10 PUS_2_100K_Ohm_Pull_Up — 100K Ohm Pull Up 11 PUS_3_22K_Ohm_Pull_Up — 22K Ohm Pull Up
13 PUE	Pull / Keep Select Field Select one out of next values for pad: UART1_RX_DATA 0 PUE_0_Keeper — Keeper 1 PUE_1_Pull — Pull
12 PKE	Pull / Keep Enable Field Select one out of next values for pad: UART1_RX_DATA 0 PKE_0_Pull_Keeper_Disabled — Pull/Keeper Disabled 1 PKE_1_Pull_Keeper_Enabled — Pull/Keeper Enabled
11 ODE	Open Drain Enable Field Select one out of next values for pad: UART1_RX_DATA

Table continues on the next page...

IOMUXC_SW_PAD_CTL_PAD_UART1_RX_DATA field descriptions (continued)

Field	Description
	0 ODE_0_Open_Drain_Disabled — Open Drain Disabled 1 ODE_1_Open_Drain_Enabled — Open Drain Enabled
10–8 -	This field is reserved. Reserved
7–6 SPEED	Speed Field Select one out of next values for pad: UART1_RX_DATA 00 SPEED_0_low_50MHz_ — low(50MHz) 01 SPEED_1_medium_100MHz_ — medium(100MHz) 10 SPEED_2_medium_100MHz_ — medium(100MHz) 11 SPEED_3_max_200MHz_ — max(200MHz)
5–3 DSE	Drive Strength Field Select one out of next values for pad: UART1_RX_DATA 000 DSE_0_output_driver_disabled_ — output driver disabled; 001 DSE_1_R0_260_Ohm__3_3V_150_Ohm_1_8V_240_Ohm_for_DDR_ — R0(260 Ohm @ 3.3V, 150 Ohm@1.8V, 240 Ohm for DDR) 010 DSE_2_R0_2 — R0/2 011 DSE_3_R0_3 — R0/3 100 DSE_4_R0_4 — R0/4 101 DSE_5_R0_5 — R0/5 110 DSE_6_R0_6 — R0/6 111 DSE_7_R0_7 — R0/7
2–1 -	This field is reserved. Reserved
0 SRE	Slew Rate Field Select one out of next values for pad: UART1_RX_DATA 0 SRE_0_Slow_Slew_Rate — Slow Slew Rate 1 SRE_1_Fast_Slew_Rate — Fast Slew Rate